

ABSTRACT OF THE DISCLOSURE

Address pointers (PTFi) associated with corresponding fingers are set to the addresses of a memory means (MM) capable of storing a number of symbols larger than the maximum delay between the paths, these initial addresses being predetermined taking account of differences in the number of symbols between the paths. Under steady state conditions, a current symbol received on a first finger is stored in the said memory at the write address given by the corresponding address pointer, and the said address pointer is then incremented. Before reception of the next symbol on this first finger, the contents of the memory stored at the read addresses denoted by all the other address pointers, are extracted in sequence, these contents are summed (ADD) with the symbols present on the other fingers and, except for the sum corresponding to the last finger, these sums are stored at the same read addresses before incrementing all the other pointers, the last sum corresponding to the last finger being delivered at the output from the receiver.